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PATENT APPLICATION

Sheet 1 of 1

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			UBLICATIONS FOR		200208858-1		10/648,570		
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REFERE	NCE	DESIGNATION	U.S. P	ATEN	T DOCUMENTS				
EXAMINE INITIAL	1	DOCUMENT PUBLICATION NUMBER DATE			NAME		Pages, Columns, Lines Where Relevant Passages or Figures Appear		
MBJ	1A	2003/0060176	03/27/2003	Hei	inonen, et al.				
MBS	18	6,157,247	12/05/2000	Ab	Abdesselem, et al.				
WB?	1C	6,449,575 B2	09/10/2002	Bau	Bausch, et al.		 		
<u>rgm</u>		6,509,788 B2	01/21/2003		fiziger, et al.				
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		OTHER REF	ERENCES (includi	ng Aut	thor, Title, Date, P	ertino	ent Pages, etc.)		
YB.	THOMAS D. BURD, et al., "A Dynamic Voltage Scaled Microprocessor System", IEEE Journal of Solid-State Circuits, Vol. 35, No. 11, pp. 1571-1580, November 2000								_
mr.	1R SHEKHAR BORKAR, et al., *Parameter Variations and Impact on Circuits and Microarchitectu Circuit Research, Intel Labs, JF3-334, pp. 338-342, June 2003								
	15	;							
EXAMINER					DATE CONSIDERED				
SHINGLETON					4/29/2025				